

ABSTRACT OF THE DISCLOSURE

A signal processing circuit in a digital portable telephone apparatus having a GPS function has a phase shifter for making an I signal and a Q signal obtained by demodulating a BPSK modulated signal coincide with each other in phase and an adder for combining both the signals (I and Q signals) after the phase coincidence with each other. Therefore, a receiving IF stage normally used in a portable telephone apparatus is used also as an IF stage for a GPS receiving system.

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